

	L #	Hits	Search Text	DBs
1	L5	6551	(identif\$6 detect\$3 determin\$3) near10 ((program application instruction code microcode microinstruction routine call subroutine) near10 ((defect\$3 fail\$3 fault\$3 unable disable\$3 unavailable available) near10 (hardware device function\$5 module unit element)))	USPAT; US-PGPUB
2	L7	1504	(identif\$6 detect\$3 determin\$3) near10 ((program application instruction code microcode microinstruction routine call subroutine) near10 ((defect\$3 fail\$3 fault\$3 unable disable\$3 unavailable available) near10 (hardware device function\$5 module unit element)))	EPO; JPO; DERWENT; IBM_TDB
3	L8	24	7 near30 ((replac\$5 substitut\$3 modif\$6 alter\$3 chang\$3 emulat\$3 patch\$3) near10 (instruction code microcode microinstruction routine subroutine))	EPO; JPO; DERWENT; IBM_TDB
4	L6	116	5 near30 ((replac\$5 substitut\$3 modif\$6 alter\$3 chang\$3 emulat\$3 patch\$3) near10 (instruction code microcode microinstruction routine subroutine))	USPAT; US-PGPUB
5	L9	413091	(identif\$6 detect\$3 determin\$3) near10 (program application instruction code microcode microinstruction routine call subroutine)	USPAT; US-PGPUB
6	L12	150826	(identif\$6 detect\$3 determin\$3) near10 (program application instruction code microcode microinstruction routine call subroutine)	EPO; JPO; DERWENT; IBM_TDB
7	L11	54	9 adj50 ((dynamic\$5 run) near10 ((replac\$5 substitut\$3 modif\$6 alter\$3 chang\$3 emulat\$3 patch\$3) near10 (instruction code microcode microinstruction routine subroutine)))	USPAT; US-PGPUB
8	L13	5	12 adj50 ((dynamic\$5 run) near10 ((replac\$5 substitut\$3 modif\$6 alter\$3 chang\$3 emulat\$3 patch\$3) near10 (instruction code microcode microinstruction routine subroutine)))	EPO; JPO; DERWENT; IBM_TDB
9	L14	39570	((program application instruction code microcode microinstruction routine call subroutine) near10 ((defect\$3 fail\$3 fault\$3 unable disable\$3 unavailable available) near10 (hardware device function\$5 module unit element)))	USPAT; US-PGPUB
10	L15	14	14 adj50 ((dynamic\$5 run runtime execut\$3) near10 ((replac\$5 substitut\$3 modif\$6 alter\$3 chang\$3 emulat\$3 patch\$3) near10 (instruction code microcode microinstruction routine subroutine))) not (6 11)	USPAT; US-PGPUB
11	L16	12	14 adj50 ((dynamic\$5 run runtime execut\$3) near20 ((replac\$5 substitut\$3 modif\$6 alter\$3 chang\$3 emulat\$3 patch\$3) near10 (instruction code microcode microinstruction routine subroutine))) not (6 11)	USPAT; US-PGPUB

	L #	Hits	Search Text	DBs
1	L5	6551	(identif\$6 detect\$3 determin\$3) near10 ((program application instruction code microcode microinstruction routine call subroutine) near10 ((defect\$3 fail\$3 fault\$3 unable disable\$3 unavailable available) near10 (hardware device function\$5 module unit element)))	USPAT; US-PGPUB
2	L7	1504	(identif\$6 detect\$3 determin\$3) near10 ((program application instruction code microcode microinstruction routine call subroutine) near10 ((defect\$3 fail\$3 fault\$3 unable disable\$3 unavailable available) near10 (hardware device function\$5 module unit element)))	EPO; JPO; DERWENT; IBM_TDB
3	L8	24	7 near30 ((replac\$5 substitut\$3 modif\$6 alter\$3 chang\$3 emulat\$3 patch\$3) near10 (instruction code microcode microinstruction routine subroutine))	EPO; JPO; DERWENT; IBM_TDB
4	L6	116	5 near30 ((replac\$5 substitut\$3 modif\$6 alter\$3 chang\$3 emulat\$3 patch\$3) near10 (instruction code microcode microinstruction routine subroutine))	USPAT; US-PGPUB
5	L9	413091	(identif\$6 detect\$3 determin\$3) near10 (program application instruction code microcode microinstruction routine call subroutine)	USPAT; US-PGPUB
6	L12	150826	(identif\$6 detect\$3 determin\$3) near10 (program application instruction code microcode microinstruction routine call subroutine)	EPO; JPO; DERWENT; IBM_TDB
7	L11	54	9 adj50 ((dynamic\$5 run) near10 ((replac\$5 substitut\$3 modif\$6 alter\$3 chang\$3 emulat\$3 patch\$3) near10 (instruction code microcode microinstruction routine subroutine)))	USPAT; US-PGPUB
8	L13	5	12 adj50 ((dynamic\$5 run) near10 ((replac\$5 substitut\$3 modif\$6 alter\$3 chang\$3 emulat\$3 patch\$3) near10 (instruction code microcode microinstruction routine subroutine)))	EPO; JPO; DERWENT; IBM_TDB

	Document ID	U	Title	Current OR
1	US 20040 01988 4 A1	<input type="checkbox"/>	Eliminating cold register store/restores within hot function prolog/epilogs	717/154
2	US 20030 20053 7 A1	<input checked="" type="checkbox"/>	Apparatus and method for using database knowledge to optimize a computer program	717/151
3	US 20030 19579 3 A1	<input checked="" type="checkbox"/>	Automated online design and analysis of marketing research activity and data	705/10
4	US 20030 13666 3 A1	<input checked="" type="checkbox"/>	Method and apparatus for monitoring plasma processing operations	204/192 .13
5	US 20030 10995 9 A1	<input checked="" type="checkbox"/>	Device for controlling robot behavior and method for controlling it	700/245
6	US 20030 02972 0 A1	<input checked="" type="checkbox"/>	Method & apparatus for monitoring plasma processing operations	204/298 .01
7	US 20030 02481 0 A1	<input checked="" type="checkbox"/>	Method & apparatus for monitoring plasma processing operations	204/298 .01
8	US 20030 01984 0 A1	<input checked="" type="checkbox"/>	Method & apparatus for monitoring plasma processing operations	216/60
9	US 20020 19667 8 A1	<input checked="" type="checkbox"/>	Eliminating store/restores within hot function prolog/epilogs using volatile regosters	365/200
10	US 20020 13381 0 A1	<input checked="" type="checkbox"/>	Method for hybrid processing of software instructions of an emulated computer system	717/138
11	US 20020 09375 0 A1	<input checked="" type="checkbox"/>	Modulation system	360/29
12	US 20020 08564 4 A1	<input checked="" type="checkbox"/>	Modulation system	375/271
13	US 20020 05093 5 A1	<input checked="" type="checkbox"/>	Modulation method, modulator, transmission apparatus and recording medium	341/58
14	US 65057 78 B1	<input checked="" type="checkbox"/>	Optical reader with selectable processing characteristics for reading data in multiple formats	235/462 .25
15	US 64929 20 B2	<input checked="" type="checkbox"/>	Modulation method, modulator, transmission apparatus and recording medium	341/106
16	US 64697 13 B2	<input checked="" type="checkbox"/>	Method, system and computer program product for dynamic language switching via messaging	345/740
17	US 64198 01 B1	<input checked="" type="checkbox"/>	Method and apparatus for monitoring plasma processing operations	204/192 .13
18	US 63834 02 B1	<input checked="" type="checkbox"/>	Method and apparatus for monitoring plasma processing operations	216/60
19	US 63697 24 B1	<input checked="" type="checkbox"/>	Modulating apparatus and method, demodulating apparatus and method, and transmission medium	341/59

	Document ID	U	Title	Current OR
20	US 63409 38 B1	<input checked="" type="checkbox"/>	Demodulating device, demodulating method and supply medium with predetermined error length	341/58
21	US 62757 40 B1	<input checked="" type="checkbox"/>	Method and apparatus for monitoring plasma processing operations	700/108
22	US 62692 78 B1	<input checked="" type="checkbox"/>	Method and apparatus for monitoring plasma processing operations	700/121
23	US 62614 70 B1	<input checked="" type="checkbox"/>	Method and apparatus for monitoring plasma processing operations	216/60
24	US 62547 17 B1	<input checked="" type="checkbox"/>	Method and apparatus for monitoring plasma processing operations	156/345 .24
25	US 62464 73 B1	<input checked="" type="checkbox"/>	Method and apparatus for monitoring plasma processing operations	356/316
26	US 62237 55 B1	<input checked="" type="checkbox"/>	Method and apparatus for monitoring plasma processing operations	134/1.1
27	US 62216 79 B1	<input checked="" type="checkbox"/>	Method and apparatus for monitoring plasma processing operations	438/7
28	US 61928 26 B1	<input checked="" type="checkbox"/>	Method and apparatus for monitoring plasma processing operations	118/723 AN
29	US 61782 88 B1	<input checked="" type="checkbox"/>	Reproduction device	386/96
30	US 61764 29 B1	<input checked="" type="checkbox"/>	Optical reader with selectable processing characteristics for reading data in multiple formats	235/462 .25
31	US 61699 33 B1	<input checked="" type="checkbox"/>	Method and apparatus for monitoring plasma processing operations	700/121
32	US 61653 12 A	<input checked="" type="checkbox"/>	Method and apparatus for monitoring plasma processing operations	156/345 .24
33	US 61574 47 A	<input checked="" type="checkbox"/>	Method and apparatus for monitoring plasma processing operations	356/316
34	US 61340 05 A	<input checked="" type="checkbox"/>	Method and apparatus for monitoring plasma processing operations	356/451
35	US 61325 77 A	<input checked="" type="checkbox"/>	Method and apparatus for monitoring plasma processing operations	204/298 .32
36	US 61239 83 A	<input checked="" type="checkbox"/>	Method and apparatus for monitoring plasma processing operations	427/10
37	US 60903 02 A	<input checked="" type="checkbox"/>	Method and apparatus for monitoring plasma processing operations	216/60
38	US 60773 86 A	<input checked="" type="checkbox"/>	Method and apparatus for monitoring plasma processing operations	156/345 .24
39	US 60496 72 A	<input checked="" type="checkbox"/>	Microprocessor with circuits, systems, and methods for operating with patch micro-operation codes and patch microinstruction codes stored in multi-purpose memory structure	717/168
40	US 59681 54 A	<input checked="" type="checkbox"/>	Distributed priority arbitrating method and system in multi-point serial networks with different transmission rates	710/119
41	US 59371 85 A	<input checked="" type="checkbox"/>	Method and system for device virtualization based on an interrupt request in a DOS-based environment	703/24

	Document ID	U	Title	Current OR
42	US 58023 67 A	<input checked="" type="checkbox"/>	Method and system for transparently executing code using a surrogate process	719/332
43	US 58016 14 A	<input checked="" type="checkbox"/>	Vehicle starting control device	340/425 .5
44	US 56733 15 A	<input checked="" type="checkbox"/>	Method and system for software asset usage detection and management	705/59
45	US 53216 75 A	<input checked="" type="checkbox"/>	Pit spacing in the servo field of an optical disk for speed and direction detection and data retrieval	369/30.2
46	US 52008 22 A	<input checked="" type="checkbox"/>	Arrangement for and method of processing data, especially for identifying and verifying airing of television broadcast programs	725/22
47	US 51990 83 A	<input checked="" type="checkbox"/>	Image data processing method and system for giving identification labels to areas of connected black picture elements	382/180
48	US 51154 97 A	<input checked="" type="checkbox"/>	Optically intraconnected computer employing dynamically reconfigurable holographic optical element	712/200
49	US 49964 68 A	<input checked="" type="checkbox"/>	Automated guided vehicle	318/587
50	US 48476 77 A	<input checked="" type="checkbox"/>	Video telecommunication system and method for compressing and decompressing digital color video data	375/240 .01
51	US 48462 97 A	<input checked="" type="checkbox"/>	Automated guided vehicle	180/169
52	US 47904 02 A	<input checked="" type="checkbox"/>	Automated guided vehicle	180/169
53	US 40978 07 A	<input checked="" type="checkbox"/>	Automatic equalizing method and system	375/232
54	US 39480 54 A	<input type="checkbox"/>	Steam turbine blade protection system and method especially for electric power plants	60/660

	Docum ent ID	U	Title	Current OR
1	JP 11212 798 A	<input type="checkbox"/>	LANGUAGE PROCESSING METHOD, LANGUAGE PROCESSOR AND STORAGE MEDIUM RECORDED WITH LANGUAGE PROCESSING PROGRAM	
2	JP 06337 786 A	<input checked="" type="checkbox"/>	METHOD OR DEVICE FOR CONVERTING EXECUTION INSTRUCTION GROUP	
3	EP 13168 87 A2	<input checked="" type="checkbox"/>	System and method for dynamically patching code	
4	EP 92659 6 A	<input checked="" type="checkbox"/>	Instruction reordering method for reducing processor power consumption	
5	EP 30489 1 A	<input type="checkbox"/>	Controlling system for computer input in multi-mode state - has input events e.g. from keyboard processed by various subroutines e.g. translation table	

	Document ID	U	Title	Current OR
1	US 20030 05871 1 A1	<input type="checkbox"/>	Method for integrating imperfect semiconductor memory devices in data processing apparatus	365/200
2	US 20010 04746 9 A1	<input type="checkbox"/>	Emptying packed data state during execution of packed data instructions	712/229
3	US 62666 86 B1	<input type="checkbox"/>	Emptying packed data state during execution of packed data instructions	708/204
4	US 61887 20 B1	<input type="checkbox"/>	Modulation and signaling converter	375/222
5	US 61709 97 B1	<input type="checkbox"/>	Method for executing instructions that operate on different data types stored in the same single logical register file	712/217
6	US 60028 81 A	<input type="checkbox"/>	Coprocessor data access control	712/34
7	US 59408 59 A	<input type="checkbox"/>	Emptying packed data state during execution of packed data instructions	711/147
8	US 58931 57 A	<input type="checkbox"/>	Blocking symbol control in a computer system to serialize accessing a data resource by simultaneous processor requests	711/150
9	US 58570 96 A	<input type="checkbox"/>	Microarchitecture for implementing an instruction to clear the tags of a stack reference register file	712/229
10	US 58527 26 A	<input type="checkbox"/>	Method and apparatus for executing two types of instructions that specify registers of a shared logical register file in a stack and a non-stack referenced manner	712/200
11	US 58357 48 A	<input type="checkbox"/>	Method for executing different sets of instructions that cause a processor to perform different data type operations on different physical registers files that logically appear to software as a single aliased register file	712/217
12	US 57782 11 A	<input type="checkbox"/>	Emulating a delayed exception on a digital computer having a corresponding precise exception mechanism	703/26
13	US 57015 08 A	<input type="checkbox"/>	Executing different instructions that cause different data type operations to be performed on single logical register file	712/23
14	US 48751 54 A	<input type="checkbox"/>	Microcomputer with disconnected, open, independent, bimemory architecture, allowing large interacting, interconnected multi-microcomputer parallel systems accomodating multiple levels of programmer defined heirarchy	712/38

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1	L5	6551	(identif\$6 detect\$3 determin\$3) near10 ((program application instruction code microcode microinstruction routine call subroutine) near10 ((defect\$3 fail\$3 fault\$3 unable disable\$3 unavailable available) near10 (hardware device function\$5 module unit element)))	USPAT; US-PGPUB
2	L7	1504	(identif\$6 detect\$3 determin\$3) near10 ((program application instruction code microcode microinstruction routine call subroutine) near10 ((defect\$3 fail\$3 fault\$3 unable disable\$3 unavailable available) near10 (hardware device function\$5 module unit element)))	EPO; JPO; DERWENT; IBM_TDB
3	L8	24	7 near30 ((replac\$5 substitut\$3 modif\$6 alter\$3 chang\$3 emulat\$3 patch\$3) near10 (instruction code microcode microinstruction routine subroutine))	EPO; JPO; DERWENT; IBM_TDB
4	L6	116	5 near30 ((replac\$5 substitut\$3 modif\$6 alter\$3 chang\$3 emulat\$3 patch\$3) near10 (instruction code microcode microinstruction routine subroutine))	USPAT; US-PGPUB
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8	L13	5	12 adj50 ((dynamic\$5 run) near10 ((replac\$5 substitut\$3 modif\$6 alter\$3 chang\$3 emulat\$3 patch\$3) near10 (instruction code microcode microinstruction routine subroutine)))	EPO; JPO; DERWENT; IBM_TDB
9	L14	39570	((program application instruction code microcode microinstruction routine call subroutine) near10 ((defect\$3 fail\$3 fault\$3 unable disable\$3 unavailable available) near10 (hardware device function\$5 module unit element)))	USPAT; US-PGPUB
10	L15	14	14 adj50 ((dynamic\$5 run runtime execut\$3) near10 ((replac\$5 substitut\$3 modif\$6 alter\$3 chang\$3 emulat\$3 patch\$3) near10 (instruction code microcode microinstruction routine subroutine))) not (6 11)	USPAT; US-PGPUB
11	L22	130733	(identif\$6 detect\$3 determin\$3 check\$3) near10 ((program application instruction code microcode microinstruction routine call subroutine) near10 ((defect\$3 fail\$3 fault\$3 unable disable\$3 unavailable available) (hardware device function\$5 module unit element)))	USPAT; US-PGPUB
12	L24	475	22 adj50 ((replac\$5 substitut\$3 modif\$6 alter\$3 chang\$3 emulat\$3 patch\$3) near10 (instruction code microcode microinstruction routine subroutine))	USPAT; US-PGPUB
13	L27	45404	(identif\$6 detect\$3 determin\$3 check\$3) near10 ((program application instruction code microcode microinstruction routine call subroutine) near10 ((defect\$3 fail\$3 fault\$3 unable disable\$3 unavailable available) (hardware device function\$5 module unit element)))	EPO; JPO; DERWENT; IBM_TDB
14	L28	84	27 adj50 ((replac\$5 substitut\$3 modif\$6 alter\$3 chang\$3 emulat\$3 patch\$3) near10 (instruction code microcode microinstruction routine subroutine)) not (8 13)	EPO; JPO; DERWENT; IBM_TDB
15	L26	139	24 and ((replac\$5 substitut\$3 modif\$6 alter\$3 chang\$3 emulat\$3 patch\$3) near10 (instruction code microcode microinstruction routine subroutine)).ab,ti. not (6 11)	USPAT; US-PGPUB

	Document ID	U	Title	Current OR
1	JP 20032 42757 A	<input type="checkbox"/>	DISK PLAYBACK DEVICE	
2	JP 20020 06891 A	<input checked="" type="checkbox"/>	DEVICE FOR IMPROVING SOUND SIGNAL QUALITY	
3	JP 20013 44185 A	<input checked="" type="checkbox"/>	INFORMATION PROCESSING DEVICE AND I/O EMULATION CONTROL METHOD	
4	JP 20012 85462 A	<input checked="" type="checkbox"/>	DEVICE FOR RECORDING AND REPRODUCING VOICE COMMUNICATION	
5	JP 20012 83012 A	<input checked="" type="checkbox"/>	DATA DISPLAY DEVICE	
6	JP 20011 88676 A	<input checked="" type="checkbox"/>	DATA-DRIVEN TYPE COMPUTER, AND DATA-DRIVEN TYPE COMPUTER SYSTEM	
7	JP 20011 84341 A	<input checked="" type="checkbox"/>	COMPILER AND RECORDING MEDIUM	
8	JP 20011 35019 A	<input checked="" type="checkbox"/>	OPTICAL DISK RECORDING/REPRODUCING DEVICE	
9	JP 20003 44055 A	<input checked="" type="checkbox"/>	REMOTE CONTROL DEVICE FOR ON-VEHICLE EQUIPMENT	
10	JP 20002 87285 A	<input checked="" type="checkbox"/>	REMOTE CONTROL RECEIVER	
11	JP 20002 59427 A	<input checked="" type="checkbox"/>	PROGRAM CONVERTER	
12	JP 20002 15039 A	<input checked="" type="checkbox"/>	PROGRAM GENERATING DEVICE BASED UPON RULE, PROGRAM GENERATING METHOD, AND RECORDING MEDIUM WHERE PROGRAM FOR PROGRAM GENERATION IS RECORDED	
13	JP 20001 04606 A	<input checked="" type="checkbox"/>	AUTOMATIC CONTROL DEVICE FOR LOW IDLE SPEED OF ENGINE	
14	JP 20000 42268 A	<input checked="" type="checkbox"/>	EMBROIDERY PUNCHING METHOD	
15	JP 10254 967 A	<input checked="" type="checkbox"/>	DATA DISPLAY DEVICE	
16	JP 10232 782 A	<input checked="" type="checkbox"/>	PROGRAM LOADING DEVICE FOR EMULATOR FOR MICROCOMPUTER	
17	JP 10149 237 A	<input checked="" type="checkbox"/>	SEMICONDUCTOR CIRCUIT	
18	JP 10011 468 A	<input checked="" type="checkbox"/>	VOICE OUTPUT CONTROLLER	

	Document ID	U	Title	Current OR
19	JP 08180 264 A	<input checked="" type="checkbox"/>	BURGLAR PREVENTING DEVICE FOR VEHICLE	
20	JP 08123 919 A	<input checked="" type="checkbox"/>	NONCONTACT IC CARD SYSTEM AND COMMUNICATION METHOD THEREOF	
21	JP 07168 659 A	<input checked="" type="checkbox"/>	FUNCTION SETTING DEVICE	
22	JP 07143 074 A	<input checked="" type="checkbox"/>	TRANSMISSION CODE ERROR COMPENSATING DEVICE	
23	JP 06339 583 A	<input checked="" type="checkbox"/>	TOY ASSEMBLY SET EQUIPPED WITH ELEMENT WHICH OFFERS POSITIONAL INFORMATION	
24	JP 06111 137 A	<input checked="" type="checkbox"/>	MERCHANDISE SALES REGISTERING DATA PROCESSOR	
25	JP 05181 684 A	<input checked="" type="checkbox"/>	COMPILER	
26	JP 05095 351 A	<input checked="" type="checkbox"/>	SCRAMBLER AND ITS DESCRAMBLER	
27	JP 05081 014 A	<input checked="" type="checkbox"/>	DEVICE FOR CORRECTING MICROINSTRUCTION HAVING ERROR	
28	JP 02112 083 A	<input checked="" type="checkbox"/>	PORTABLE SCANNING DEVICE PROVIDED WITH MONITORING TAG INACTIVATING FUNCTION	
29	JP 02076 047 A	<input checked="" type="checkbox"/>	CIRCUIT FOR DETECTING UNDEFINED ADDRESS AREA CALLING	
30	JP 01259 450 A	<input checked="" type="checkbox"/>	DOCUMENT PROCESSOR	
31	JP 01084 964 A	<input checked="" type="checkbox"/>	SYSTEM FOR SUBSTITUTE RESPONSE	
32	JP 01068 805 A	<input checked="" type="checkbox"/>	PROCESS CONTROLLER	
33	JP 63024 425 A	<input checked="" type="checkbox"/>	TEST COMPREHENSIVE STATE DETECTION SYSTEM	
34	JP 62248 039 A	<input checked="" type="checkbox"/>	CONTROL PROGRAM ANALYZER	
35	JP 62160 558 A	<input checked="" type="checkbox"/>	METHOD FOR CHECKING FUNCTION OF TRANSMISSION CONTROL PROCEDURE IMPLEMENTING SOFTWARE	
36	JP 62079 141 A	<input checked="" type="checkbox"/>	AUTOMATIC PAPER FEEDER	
37	JP 58077 336 A	<input checked="" type="checkbox"/>	LINE SWITCHING SYSTEM IN ON-LINE SYSTEM	
38	JP 57143 640 A	<input checked="" type="checkbox"/>	DATA PROCESSING DEVICE	
39	EP 12110 78 A1	<input checked="" type="checkbox"/>	Thermal monitoring system for determining nozzle health	
40	WO 17553 5 A2	<input checked="" type="checkbox"/>	METHOD FOR CONTROLLING MECHANISMS AND TECHNICAL SYSTEMS, A CORRESPONDING DEVICE AND CONTROL SOFTWARE	
41	EP 92659 4 A1	<input checked="" type="checkbox"/>	Method of using primary and secondary processors	

	Document ID	U	Title	Current OR
42	WO 99050 15 A2	<input checked="" type="checkbox"/>	LOCOMOTIVE REMOTE CONTROL SYSTEM	
43	EP 74364 2 A2	<input checked="" type="checkbox"/>	Data recording/reproducing apparatus corresponding to a plurality of error correcting system and a data recording medium	
44	EP 68140 3 A1	<input checked="" type="checkbox"/>	Coupling device for coupling a terminal to a generating device, system and methods.	
45	EP 29428 7 A2	<input checked="" type="checkbox"/>	Fault-tolerant, error-correcting storage system.	
46	EP 23902 3 A1	<input checked="" type="checkbox"/>	Arrangement for processing branch instructions in pipelined data-processing systems.	
47	EP 21338 3 A1	<input checked="" type="checkbox"/>	Method and device for monitoring the operation of optical smoke detectors.	
48	EP 14928 4 A1	<input checked="" type="checkbox"/>	Method for locating faults.	
49	US 20030 20470 5 A	<input checked="" type="checkbox"/>	Data processing apparatus in computer system, has prefetch unit to retrieve instructions in determined target address, when predetermined software code branch operation is predicted to cause change in instruction flow	
50	WO 20030 85667 A	<input checked="" type="checkbox"/>	Channel code generating method for recording devices e.g. coders, involves determining running digital sum of stream of output words, and replacing sequence of Q m-bit code words by sequence of Q m-bit replacement code words	
51	TW 53505 5 A	<input checked="" type="checkbox"/>	Application program with self-detection and self-repairing functions - preventing damages from computer virus to the application program	
52	WO 20018 3007 A	<input checked="" type="checkbox"/>	Closed loop system for controlling administration of medication to patient, comprises sensor package, medication delivery unit, and medication delivery controller	
53	KR 20010 75799 A	<input checked="" type="checkbox"/>	Chain type portable locking device	
54	JP 20011 75921 A	<input checked="" type="checkbox"/>	Paper sheet processing system for record book, compares code of current record book with code of previous record book, and accordingly changes information of previous record book to current record book	
55	RU 21288 49 C	<input checked="" type="checkbox"/>	Radioisotope altimeter	
56	US 57373 62 A	<input checked="" type="checkbox"/>	Code tracking loop in receiver double delay locked loop code tracking system - has code generating unit and correlation unit to provide loop for spreading linear section of energy detecting area, and using difference of preceding time code and delayed time code, according to receiver environment change	
57	EP 71899 8 A	<input checked="" type="checkbox"/>	De-spread code phase detector for SS receiver having RAKE synthesising demodulating function - uses search correlator to obtain correlation results of pilot signal subjected to spread spectrum demodulation and de-spread code while sequentially change latter	
58	JP 07219 781 A	<input checked="" type="checkbox"/>	Program translation device transforms program to intermediate expression, replaces temporary variable in expression with address variable, detects replaced sentence and deletes replaced sentence to generate object code	
59	US 53455 67 A	<input checked="" type="checkbox"/>	Modifying program status word system mask, access key, and address space code system with overlap enabled - has register device coupled to execution unit and to queue device for storing standardised program status word, which is program status word in subsystem	

	Document ID	U	Title	Current OR
60	GB 22745 27 A	<input checked="" type="checkbox"/>	Microprocessor for parallel processing of multiple instructions - executes successive instructions in processing unit when parallel processing of branch instruction and other instruction in one cycle	
61	EP 55197 2 A	<input checked="" type="checkbox"/>	Automatic train controller - uses line section repeaters transmitting reference control characteristic to train plus modifications for other train types	
62	JP 05102 953 A	<input checked="" type="checkbox"/>	Bit synchronising device - has edge detection routine for counting number of sampling pulses until bit synchronous signal in reception signal attains each code change point NoAbstract	
63	JP 05029 950 A	<input checked="" type="checkbox"/>	Code quantity control device to transmit coded moving image - determines step size according to accumulated surplus code quantity and surplus code quantity at present frame to eliminate change useless step size NoAbstract	
64	GB 22573 70 A	<input checked="" type="checkbox"/>	Inflatable bouncing play device e.g. bouncing castles - comprises inflatable framework with uninflated regions spanning framework for use in erected state	
65	JP 04291 826 A	<input checked="" type="checkbox"/>	Code transferring device sending coded result having identification code - conserves high transferring efficiency by making data quantity generated against performance change of information source small NoAbstract	
66	EP 49452 5 A	<input checked="" type="checkbox"/>	Telephone with calling line identification - comprises memory storing telephone numbers together with associated operating instruction and interrupts normal operation correspondingly	
67	EP 48345 5 A	<input checked="" type="checkbox"/>	Single request arbitration for common data bus - determines if only one communication module is contending for bus and gives access within two bus clocks	
68	EP 46923 9 A	<input checked="" type="checkbox"/>	Detection and correction appts. for faulty processor operational codes - detects faulty instructions using logic nor circuit and replaces them with error-free micro-code from programmable array	
69	GB 22301 19 A	<input checked="" type="checkbox"/>	Conditional execution speed-up on synchronising instructions - involves replacement of synchronisation instruction with null instruction where followed by floating point instruction	
70	DE 39249 01 C	<input checked="" type="checkbox"/>	Automatic film rewinder for photographic camera - has sensor for presence of film, device for sensing length of rewound film using frame and perforation counters	
71	GB 22213 21 A	<input checked="" type="checkbox"/>	Disabling indicator unit for electronically controlled camera - includes mechanism to disable power supply, in non-picture taking state, when detector fails to sense film in camera	
72	FR 26337 33 A	<input checked="" type="checkbox"/>	Auto-focus zoom photographic camera with electronic control - has actuated by electric motor taking lens and automatically moved from macro to zoom position according to subject range	
73	DE 39216 18 A	<input checked="" type="checkbox"/>	Zoom lens position detector esp. for automatic camera - provides digital signal read-out from servo driven encoder to supply microcomputer and control system	
74	EP 32795 0 A	<input checked="" type="checkbox"/>	Addressing memory in excess of size of instruction operand - extending access memory from control instruction giving greater memory access range without increasing area of ROM	
75	EP 29789 0 A	<input checked="" type="checkbox"/>	Data processor fault condition response system - generates boolean valve indicating operation validity for storage at predetermined register position to initiate fault condition signal	
76	WO 87018 35 A	<input checked="" type="checkbox"/>	Secure computer system with screw terminal and keyboard - display information on screw from which code can be derived when system code required to initiate or regain access	
77	EP 21064 0 A	<input checked="" type="checkbox"/>	Input-output control system for virtual machine system - allows switching during VM run, between direct and indirect I-O execution modes	
78	EP 20766 6 A	<input checked="" type="checkbox"/>	Moving string of bytes instruction in reduced instruction set computer - using compiler to generate optimised sequences instead of hardware to perform operations and pre-compute operands	
79	DE 35095 62 A	<input checked="" type="checkbox"/>	Anti-theft protection for car radio - prevents operation of radio upon its removal until input of correct code word	

	Document ID	U	Title	Current OR
80	DE 36847 88 G	<input checked="" type="checkbox"/>	Correction of multiple errors in data word - using complement and retry technique to locate hard faults with subsequent repeated change of bits to remove error	
81	SU 11985 18 A	<input checked="" type="checkbox"/>	Random binary number generator with accurate output - has freq. divider at inputs of delay elements taken to binary counter, rest input of up-down counter to non-equivalence elements	
82	EP 10496 3 A	<input checked="" type="checkbox"/>	Delta modulation system for television signals - uses two sets of quantisation characteristics allowing for sharp contrast differences between two image points	
83	EP 59018 A	<input checked="" type="checkbox"/>	Multi-programmed data processing system with dynamic task assignment - requiring no centralised interrogation of all data processing devices	
84	BE 88909 0 A	<input checked="" type="checkbox"/>	Electromagnetic lock with magnetic key and detectors in barrel - uses key container magnetised cylinder to correspond to magnetic detectors in barrel and relays to connect inputs to logic circuit	

	Document ID	U	Title	Current OR
1	US 20040 06866 2 A1	<input type="checkbox"/>	System and method having an antivirus virtual scanning processor with plug-in functionalities	713/200
2	US 20040 06460 4 A1	<input checked="" type="checkbox"/>	Filter driver for blocking access by host to devices	710/36
3	US 20040 05024 4 A1	<input checked="" type="checkbox"/>	Gas pressure actuator capable of stably driving and controlling its slider, and method for controlling the gas pressure actuator	91/363R
4	US 20040 04680 6 A1	<input checked="" type="checkbox"/>	Task based polymorphic graphical program function nodes	345/853
5	US 20040 04679 6 A1	<input checked="" type="checkbox"/>	Visual field changing method	345/767
6	US 20040 02956 5 A1	<input checked="" type="checkbox"/>	Voice response unit, method thereof and telephone communication system	455/411
7	US 20040 02034 8 A1	<input checked="" type="checkbox"/>	Musical composition data editing apparatus, musical composition data distributing apparatus, and program for implementing musical composition data editing method	84/609
8	US 20040 01977 0 A1	<input checked="" type="checkbox"/>	Optimization apparatus, compiler program, optimization method and recording medium	712/227
9	US 20040 01976 6 A1	<input checked="" type="checkbox"/>	Program translator and processor	712/23
10	US 20030 23702 8 A1	<input checked="" type="checkbox"/>	Method and a system for evaluating the reliability of a program in an electronic device, and an electronic device	714/38
11	US 20030 23318 5 A1	<input checked="" type="checkbox"/>	Gear-shift control apparatus for automatic transmission and gear-shift control method	701/55
12	US 20030 19563 9 A1	<input checked="" type="checkbox"/>	Custom function blocks for use with process control systems	700/19
13	US 20030 18801 8 A1	<input checked="" type="checkbox"/>	Technique to accelerate learning rate on linearly sorted lookup tables for high speed switches/routers	709/242
14	US 20030 15873 0 A1	<input checked="" type="checkbox"/>	Method and apparatus for embedding data in and extracting data from voice code	704/222
15	US 20030 15407 3 A1	<input checked="" type="checkbox"/>	Method, apparatus and system for embedding data in and extracting data from encoded voice code	704/207
16	US 20030 14275 7 A1	<input checked="" type="checkbox"/>	Modulation apparatus/method, demodulation apparatus/method and program presenting medium	375/295
17	US 20030 11739 1 A1	<input checked="" type="checkbox"/>	SYSTEM, METHOD, AND COMPUTER PROGRAM PRODUCT FOR REAL-TIME SHADING OF COMPUTER GENERATED IMAGES	345/419

	Document ID	U	Title	Current OR
18	US 20030 10143 1 A1	<input checked="" type="checkbox"/>	System and method for dynamically replacing code	717/111
19	US 20030 09377 6 A1	<input checked="" type="checkbox"/>	Memory address prediction under emulation	717/138
20	US 20030 08885 7 A1	<input checked="" type="checkbox"/>	Method for facilitating application development	717/137
21	US 20030 05111 9 A1	<input checked="" type="checkbox"/>	Scheme for implementing breakpoints for on-chip ROM code patching	711/210
22	US 20030 04081 4 A1	<input checked="" type="checkbox"/>	Method for controlling mechanisms and technical systems a corresponding device and control software	700/42
23	US 20030 03359 2 A1	<input checked="" type="checkbox"/>	Software debugger and software development support system	717/128
24	US 20030 02396 4 A1	<input checked="" type="checkbox"/>	System and method for compacting field upgradeable wireless communication device software code sections	717/172
25	US 20030 00515 8 A1	<input checked="" type="checkbox"/>	Distributed program relocation for a computer system	709/245
26	US 20020 17837 5 A1	<input checked="" type="checkbox"/>	Method and system for protecting against malicious mobile code	713/200
27	US 20020 14771 8 A1	<input checked="" type="checkbox"/>	System and method for optimization of shared data	707/8
28	US 20020 11888 6 A1	<input checked="" type="checkbox"/>	Video transmission and receiving method and apparatus using radio communication instrument	382/250
29	US 20020 09193 4 A1	<input checked="" type="checkbox"/>	Detection of decryption to identify encrypted virus	713/188
30	US 20020 07836 8 A1	<input checked="" type="checkbox"/>	Detection of polymorphic virus code using dataflow analysis	713/200
31	US 20020 07332 3 A1	<input checked="" type="checkbox"/>	Detection of suspicious privileged access to restricted computer resources	713/188
32	US 20020 06582 2 A1	<input checked="" type="checkbox"/>	Structured document compressing apparatus and method, record medium in which a structured document compressing program is stored, structured document decompressing apparatus and method, record medium in which a structured document decompressing program is stored, and structured document processing system	707/7
33	US 20020 06411 6 A1	<input checked="" type="checkbox"/>	Information recording apparatus	369/53. 37
34	US 20020 06374 5 A1	<input checked="" type="checkbox"/>	Thermal monitoring system for determining nozzle health	347/19

	Document ID	U	Title	Current OR
35	US 20020 05738 2 A1	<input checked="" type="checkbox"/>	Television signal reception apparatus	348/731
36	US 20020 02659 6 A1	<input checked="" type="checkbox"/>	Circuit and method of generating process clock for low power consumption CDMA modem chip design	713/322
37	US 20010 00712 5 A1	<input checked="" type="checkbox"/>	Computer system with debug facility	712/226
38	US 20010 00712 4 A1	<input checked="" type="checkbox"/>	Program modification device	711/165
39	US 67150 51 B2	<input checked="" type="checkbox"/>	Program modification device	711/165
40	US 67150 43 B1	<input checked="" type="checkbox"/>	Method and system for providing memory-based device emulation	711/154
41	US 67150 36 B1	<input checked="" type="checkbox"/>	Method, system, and data structures for transferring blocks of data from a storage device to a requesting application	711/118
42	US 67116 67 B1	<input checked="" type="checkbox"/>	Microprocessor configured to translate instructions from one instruction set to another, and to store the translated instructions	712/35
43	US 66980 13 B1	<input checked="" type="checkbox"/>	Real time monitoring system for tracking and documenting changes made by programmer's during maintenance or development of computer readable code on a line by line basis and/or by point of focus	717/127
44	US 66786 66 B1	<input checked="" type="checkbox"/>	Method of conducting anti-fraud electronic bank security transactions having price-date-time variables and calculating apparatus thereof	705/66
45	US 66778 66 B2	<input checked="" type="checkbox"/>	Modulation apparatus/method, demodulation apparatus/method and program presenting medium	341/59
46	US 66683 73 B1	<input checked="" type="checkbox"/>	System, apparatus and method for expanding the range of decimal numbers of any length in existing data bases and computer programs	717/159
47	US 66656 71 B2	<input checked="" type="checkbox"/>	System and method for optimization of shared data	707/8
48	US 66586 45 B1	<input checked="" type="checkbox"/>	METHOD FOR AUTOMATICALLY GENERATING CORRECTED PROGRAM INHERITED PROGRAM, AUTOMATIC PROGRAM GENERATION APPARATUS, AND STORAGE MEDIUM STORING PROGRAM FOR AUTOMATICALLY GENERATING CORRECTED PROGRAM INHERITED PROGRAM	717/106
49	US 66585 60 B1	<input checked="" type="checkbox"/>	Program translator and processor	712/245
50	US 66576 24 B2	<input checked="" type="checkbox"/>	System, method, and computer program product for real-time shading of computer generated images	345/426
51	US 66267 56 B2	<input checked="" type="checkbox"/>	Amusement game system and a computer-readable storage medium	463/4
52	US 66063 79 B2	<input checked="" type="checkbox"/>	Methods and systems for collapsing signal transfer point (STP) infrastructure in a signaling network	379/221 .1
53	US 66041 93 B1	<input checked="" type="checkbox"/>	Processor in which register number translation is carried out	712/228
54	US 64965 41 B1	<input checked="" type="checkbox"/>	Modulation apparatus/method, demodulation apparatus/method and program presenting medium	375/253
55	US 64670 84 B1	<input checked="" type="checkbox"/>	Systems and methods for reprogramming an embedded device with program code using relocatable program code	717/136

	Document ID	U	Title	Current OR
56	US 64609 64 B2	<input checked="" type="checkbox"/>	Thermal monitoring system for determining nozzle health	347/19
57	US 64387 30 B1	<input checked="" type="checkbox"/>	RTL code optimization for resource sharing structures	716/1
58	US 64258 22 B1	<input checked="" type="checkbox"/>	Music game machine with selectable controller inputs	463/7
59	US 64112 22 B1	<input checked="" type="checkbox"/>	Method of signalling a structure of a code stream	341/50
60	US 64011 37 B1	<input checked="" type="checkbox"/>	Method, apparatus, and article of manufacture for processing a virtual call in a multi-threaded program	719/315
61	US 63570 08 B1	<input checked="" type="checkbox"/>	Dynamic heuristic method for detecting computer viruses using decryption exploration and evaluation phases	713/200
62	US 63492 02 B1	<input checked="" type="checkbox"/>	Method of storing and transmitting markup language documents in a mobile radio communications system	455/412 .1
63	US 63345 72 B1	<input checked="" type="checkbox"/>	Bar-code reader	235/462 .27
64	US 63336 84 B1	<input checked="" type="checkbox"/>	Security device for portable computer and method thereof	340/7.2
65	US 63305 49 B1	<input checked="" type="checkbox"/>	Protected shareware	705/51
66	US 62304 79 B1	<input checked="" type="checkbox"/>	Method of controlling load on power plant and load control system for carrying out the same	60/773
67	US 62224 77 B1	<input checked="" type="checkbox"/>	Cascade A/D converter	341/161
68	US 62198 33 B1	<input checked="" type="checkbox"/>	Method of using primary and secondary processors	717/149
69	US 62018 16 B1	<input checked="" type="checkbox"/>	Token ring speed detection	370/452
70	US 61925 17 B1	<input checked="" type="checkbox"/>	Method, apparatus, and product for improved garbage collection in a memory system through the removal of reference conflicts	717/154
71	US 61897 94 B1	<input checked="" type="checkbox"/>	Bar code reader determining unit for determining the true changing point and correcting unit for correcting the false changing point to correct changing point	235/462 .27
72	US 61122 80 A	<input checked="" type="checkbox"/>	Method and apparatus for distinct instruction pointer storage in a partitioned cache memory	711/129
73	US RE368 22 E	<input checked="" type="checkbox"/>	Moving image signal coding apparatus and coded signal decoding apparatus	382/236
74	US 61014 68 A	<input checked="" type="checkbox"/>	Apparatuses and methods for training and operating speech recognition systems	704/251
75	US 60920 43 A	<input checked="" type="checkbox"/>	Apparatuses and method for training and operating speech recognition systems	704/251
76	US 60818 86 A	<input checked="" type="checkbox"/>	Holding mechanism for changing operation modes in a pipelined computer	712/229
77	US 60730 97 A	<input checked="" type="checkbox"/>	Speech recognition system which selects one of a plurality of vocabulary models	704/251
78	US 60473 67 A	<input checked="" type="checkbox"/>	Microprocessor with improved out of order support	712/23

	Document ID	U	Title	Current OR
79	US 60471 25 A	<input checked="" type="checkbox"/>	Garbage collection system for improved use of memory by removal of reference conflicts	717/148
80	US 60354 33 A	<input checked="" type="checkbox"/>	Data recording/reproducing apparatus corresponding to a plurality of error correcting system and a data recording medium	714/764
81	US 60264 80 A	<input checked="" type="checkbox"/>	Processor having bug avoidance function and method for avoiding bug in processor	712/37
82	US 59960 71 A	<input checked="" type="checkbox"/>	Detecting self-modifying code in a pipelined processor with branch processing by comparing latched store address to subsequent target address	712/238
83	US 59565 10 A	<input checked="" type="checkbox"/>	Apparatus and method for revising computer program code	717/137
84	US 59434 93 A	<input checked="" type="checkbox"/>	Retargetable VLIW computer architecture and method of executing a program corresponding to the architecture	712/228
85	US 59271 13 A	<input checked="" type="checkbox"/>	Combination lock with code protection device	70/312
86	US 59179 42 A	<input checked="" type="checkbox"/>	Device and method for handwriting recognition with adaptive weighting of recognition data	382/189
87	US 59077 08 A	<input checked="" type="checkbox"/>	System and method for facilitating avoidance of an exception of a predetermined type in a digital computer system by providing fix-up code for an instruction in response to detection of an exception condition resulting from execution thereof	712/244
88	US 58988 97 A	<input checked="" type="checkbox"/>	Bit stream signal feature detection in a signal processing system	710/68
89	US 58976 33 A	<input checked="" type="checkbox"/>	System for converting programs and databases to correct year 2000 processing errors	707/6
90	US 58963 55 A	<input checked="" type="checkbox"/>	Data recording/reproducing apparatus corresponding to a plurality of error correcting system and a data recording medium	369/47. 24
91	US 58619 05 A	<input checked="" type="checkbox"/>	Digital television system with artificial intelligence	725/67
92	US 58549 32 A	<input checked="" type="checkbox"/>	Compiler and method for avoiding unnecessary recompilation	717/116
93	US 58506 27 A	<input checked="" type="checkbox"/>	Apparatuses and methods for training and operating speech recognition systems	704/231
94	US 58449 90 A	<input checked="" type="checkbox"/>	Transmission-reception system	380/262
95	US 58095 00 A	<input checked="" type="checkbox"/>	System for converting programs and databases to correct year 2000 processing errors	707/6
96	US 57817 50 A	<input checked="" type="checkbox"/>	Dual-instruction-set architecture CPU with hidden software emulation mode	712/209
97	US 57782 11 A	<input checked="" type="checkbox"/>	Emulating a delayed exception on a digital computer having a corresponding precise exception mechanism	703/26
98	US 57708 94 A	<input checked="" type="checkbox"/>	Parallel processing method having arithmetical conditions code based instructions substituted for conventional branches	717/149
99	US 57646 58 A	<input checked="" type="checkbox"/>	Data receiving apparatus and method	714/752
100	US 57517 43 A	<input checked="" type="checkbox"/>	Information transmission method and apparatus	714/774

	Document ID	U	Title	Current OR
101	US 57487 84 A	<input checked="" type="checkbox"/>	Moving image signal coding apparatus and coded signal decoding apparatus	382/236
102	US 57427 94 A	<input checked="" type="checkbox"/>	Emulation techniques for computer systems having mixed processor/software configurations	703/26
103	US 57373 62 A	<input checked="" type="checkbox"/>	Delay-locked loop code tracking system for receiver of a code spread communication system	375/149
104	US 57345 71 A	<input checked="" type="checkbox"/>	Method for modifying data loaded into memory cells of an electronic postage meter machine	705/400
105	US 56921 68 A	<input checked="" type="checkbox"/>	Prefetch buffer using flow control bit to identify changes of flow within the code stream	712/237
106	US 56614 69 A	<input checked="" type="checkbox"/>	Coupling device, system, and method for coupling a terminal to a generating device	340/825 .53
107	US 56341 37 A	<input checked="" type="checkbox"/>	Method and apparatus for updating system configuration based on open/closed state of computer housing cover	710/10
108	US 56197 02 A	<input checked="" type="checkbox"/>	Method and apparatus for programming registers using simplified commands	717/151
109	US 56149 60 A	<input checked="" type="checkbox"/>	Image data encoding method and device, image data reconstructing method and device, scene change detecting method and device, scene change recording device, and image data scene change record/regenerating device	348/700
110	US 55966 91 A	<input checked="" type="checkbox"/>	Computer system and method for manipulating the display of drawings	345/440
111	US 55795 11 A	<input checked="" type="checkbox"/>	Method and apparatus for checking the integrity of a complex computer installation used in the flight control of an aircraft	703/27
112	US 55748 73 A	<input checked="" type="checkbox"/>	Decoding guest instruction to directly access emulation routines that emulate the guest instructions	712/200
113	US 55662 86 A	<input checked="" type="checkbox"/>	Method and apparatus for verifying dimensional values of a drawing	345/589
114	US 55213 64 A	<input checked="" type="checkbox"/>	Product-selling-data processing apparatus having function for administering sales of article sold by the bundle and its method	235/383
115	US 55111 94 A	<input checked="" type="checkbox"/>	Processing system and processing method capable of dynamically replacing job environment	718/104
116	US 54816 84 A	<input checked="" type="checkbox"/>	Emulating operating system calls in an alternate instruction set using a modified code segment descriptor	712/212
117	US 54506 13 A	<input checked="" type="checkbox"/>	Mobile communications equipment which detects and notifies when it is moved into or out of a service area	455/517
118	US 54308 62 A	<input checked="" type="checkbox"/>	Emulation of CISC instructions by RISC instructions using two pipelined stages for overlapped CISC decoding and RISC execution	703/26
119	US 52901 09 A	<input checked="" type="checkbox"/>	Electronic typewriter for printing of documents having a fixed format	400/68
120	US 52626 22 A	<input checked="" type="checkbox"/>	Bar code reader	235/449
121	US 52476 91 A	<input checked="" type="checkbox"/>	System for releasing suspended execution of scalar instructions following a wait instruction immediately upon change of vector post pending signal	712/3
122	US 52351 69 A	<input checked="" type="checkbox"/>	Bar code label and bar code reader	235/462 .01

	Document ID	U	Title	Current OR
123	US 51897 31 A	<input checked="" type="checkbox"/>	Character processing system having editing function	715/524
124	US 51685 60 A	<input checked="" type="checkbox"/>	Microprocessor system private split cache tag stores with the system tag store having a different validity bit for the same data line	711/123
125	US 51092 21 A	<input checked="" type="checkbox"/>	Remote control system for door locks	340/5.2 2
126	US 49954 72 A	<input checked="" type="checkbox"/>	Steering controlling device for wheel crane	180/234
127	US 49317 23 A	<input checked="" type="checkbox"/>	Automatic test system having a "true tester-per-pin" architecture	714/730
128	US 46944 19 A	<input checked="" type="checkbox"/>	Programmable controller with direct to plant addressing	700/23
129	US 46852 90 A	<input checked="" type="checkbox"/>	Engine control with function to eliminate minute particles in exhaust gas	60/274
130	US 46213 42 A	<input checked="" type="checkbox"/>	Arbitration circuitry for deciding access requests from a multiplicity of components	710/111
131	US 45548 09 A	<input checked="" type="checkbox"/>	Combination locking device for suitcases	70/312
132	US 42544 61 A	<input checked="" type="checkbox"/>	Method and apparatus for determining linking addresses for microinstructions to be executed in a control memory of a data-processing system	712/230
133	US 41278 21 A	<input checked="" type="checkbox"/>	Channel selecting apparatus with automatic rewriting of channel identifying codes in a memory	455/182 .3
134	US 41093 11 A	<input checked="" type="checkbox"/>	Instruction execution modification mechanism for time slice controlled data processors	712/226
135	US 40794 20 A	<input checked="" type="checkbox"/>	Channel selecting apparatus	455/182 .3
136	US 40487 29 A	<input checked="" type="checkbox"/>	Electrical teaching system	434/320
137	US 39341 21 A	<input checked="" type="checkbox"/>	Counter means	377/92
138	US 36414 98 A	<input checked="" type="checkbox"/>	KEYS FOR ELECTRONIC SECURITY APPARATUS	235/487
139	US 35852 79 A	<input checked="" type="checkbox"/>	DEVICE FOR RECORDING CODED PULSES	178/23R

	L #	Hits	Search Text	DBs
1	L5	6551	(identif\$6 detect\$3 determin\$3) near10 ((program application instruction code microcode microinstruction routine call subroutine) near10 ((defect\$3 fail\$3 fault\$3 unable disable\$3 unavailable available) near10 (hardware device function\$5 module unit element)))	USPAT; US-PGPUB
2	L6	116	5 near30 ((replac\$5 substitut\$3 modif\$6 alter\$3 chang\$3 emulat\$3 patch\$3) near10 (instruction code microcode microinstruction routine subroutine))	USPAT; US-PGPUB
3	L7	1504	(identif\$6 detect\$3 determin\$3) near10 ((program application instruction code microcode microinstruction routine call subroutine) near10 ((defect\$3 fail\$3 fault\$3 unable disable\$3 unavailable available) near10 (hardware device function\$5 module unit element)))	EPO; JPO; DERWENT; IBM_TDB
4	L8	24	7 near30 ((replac\$5 substitut\$3 modif\$6 alter\$3 chang\$3 emulat\$3 patch\$3) near10 (instruction code microcode microinstruction routine subroutine))	EPO; JPO; DERWENT; IBM_TDB

	Document ID	U	Title	Current OR
1	JP 20022 88000 A	<input type="checkbox"/>	CALCULATOR SYSTEM CONTROLLING METHOD	
2	JP 05182 336 A	<input type="checkbox"/>	CONTROLLER FOR OPTICAL DISK AUTO-CHANGER	
3	JP 02138 637 A	<input type="checkbox"/>	FAULT MONITOR DEVICE	
4	JP 60196 851 A	<input type="checkbox"/>	DATA PROCESSOR	
5	JP 60072 465 A	<input type="checkbox"/>	STATE CHANGE NOTIFYING SYSTEM	
6	JP 58144 914 A	<input type="checkbox"/>	PROGRAMMABLE CONTROLLER	
7	EP 13168 87 A2	<input type="checkbox"/>	System and method for dynamically patching code	
8	EP 11115 04 A2	<input type="checkbox"/>	Compiler processing system for generating assembly program codes for a computer comprising a plurality of arithmetic units	
9	EP 10188 48 A2	<input type="checkbox"/>	Channel information processing unit for a base station	
10	EP 91731 2 A2	<input type="checkbox"/>	Wavelength division multiplex optical transmission apparatus	
11	EP 46923 9 A2	<input type="checkbox"/>	Small programmable array associated to the on-chip control store for microcode correction.	
12	US 20030 20871 0 A	<input type="checkbox"/>	Failing instruction identifying method for use during testing of electronic components, involves executing boot sequence and changing clock cycle counter value until failure instruction is identified	
13	JP 20020 32351 A	<input type="checkbox"/>	Failure information generation system for parallel processor, searches reference code dictionary using RC search mode and search sequence number determined from a reference table	
14	JP 20011 55481 A	<input type="checkbox"/>	Memory control device e.g. for laser beam printer, has AND circuit with input terminal connected to one end of chip selection terminal and output terminal connected to one ROM during program development only	
15	US 62406 33 B	<input type="checkbox"/>	Electronic device manufacturing method involves generating replacement code in response to detected defect on workpiece, to control placement of subsequent components on workpiece	
16	JP 11127 485 A	<input type="checkbox"/>	Audio and data frame transmission system for multi-media service in personal digital cellular system - replaces audio and data frames when failure of frame synchronization is detected based on error correcting code	
17	EP 76213 6 A	<input type="checkbox"/>	Paired line fault location device - uses induced coded signal which is detected by induction locator moved along route of line to detect fault by change in signal intensity	
18	US 55617 60 A	<input type="checkbox"/>	Localising field replaceable unit failures in electromechanical system - detecting element that produces Fault Symptom Code error data structures, combining probability with frequency that each FRU ID is in FSC to produce Weighted Failure Probability and reporting FRU with max. WFP as faulty	
19	EP 71700 1 A	<input type="checkbox"/>	Flexible modulating thread winding cycle esp. in ring spinning machine - using thread thickness and defect detectors to initiate thread cutting, with thread presence detectors initiating thread joining and restarting	

	Document ID	U	Title	Current OR
20	JP 06189 472 A	<input type="checkbox"/>	Power source monitoring control device for Karaoke entertainment management systems - has inverter stop instruction output part for giving barter stop command to stop operation of power source during power failure	
21	SU 16156 46 A	<input type="checkbox"/>	PCB connector bundles assembly monitor - has commutators to connect test object to measuring unit	
22	EP 38124 6 A	<input type="checkbox"/>	Pipeline microprocessor having instruction decoder unit - performs precedent decoding operation, produces decoded instruction with storage medium, and signal to initiate execution	
23	US 46355 08 A	<input type="checkbox"/>	Safety control device for automatic transmission of motor vehicle - has fault condition detector for speed and load signals and gear change instructor receiving fault representation signal	
24	DE 30047 67 B	<input type="checkbox"/>	Error detection system for digital transmission line - has ciphering circuit and alphabetic encoder for intermingling defective bits with message flow	

	Document ID	U	Title	Current OR
1	US 20040 07865 3 A1	<input type="checkbox"/>	Dynamic sparing during normal computer system operation	714/13
2	US 20040 03980 2 A1	<input checked="" type="checkbox"/>	Obtaining pieces of operating code for a network device from multiple sources	709/222
3	US 20040 02518 2 A1	<input checked="" type="checkbox"/>	Method, system and computer readable medium for a raido communication processing unit	725/61
4	US 20040 02516 5 A1	<input checked="" type="checkbox"/>	Systems and methods for extending operating system functionality for an application	719/310
5	US 20040 02217 1 A1	<input checked="" type="checkbox"/>	Recording medium for storing defect management information for recording real time data, defect managing method therefor, and real time data recording method	369/275 .3
6	US 20040 02214 8 A1	<input checked="" type="checkbox"/>	Recording medium for storing defect management information for recording real time data, defect managing method therefor, and real time data recording method	369/47. 14
7	US 20040 02214 7 A1	<input checked="" type="checkbox"/>	Recording medium for storing defect management information of recording real time data, defect managing method therefor, and real time data recording method	369/47. 14
8	US 20040 02214 6 A1	<input checked="" type="checkbox"/>	Recording medium for storing defect management information for recording real time data, defect managing method therefor, and real time data recording method	369/47. 14
9	US 20030 22738 9 A1	<input checked="" type="checkbox"/>	Smoke alarm and mounting kit	340/628
10	US 20030 19614 3 A1	<input checked="" type="checkbox"/>	Power-on state machine implementation with a counter to control the scan for products with hard-BISR memories	714/30
11	US 20030 16103 2 A1	<input checked="" type="checkbox"/>	Optical communication system and component control architectures and methods	359/341 .1
12	US 20030 15905 6 A1	<input checked="" type="checkbox"/>	Method and system for securing enablement access to a data security device	713/193
13	US 20030 13740 9 A1	<input checked="" type="checkbox"/>	Vehicle antitheft device and program	340/426 .11
14	US 20030 12690 7 A1	<input checked="" type="checkbox"/>	Failure diagnostic system for atmospheric pressure detecting device	73/1.71
15	US 20030 11542 0 A1	<input checked="" type="checkbox"/>	Methods and apparatus for implementing a chche replacement scheme	711/133
16	US 20030 10138 1 A1	<input checked="" type="checkbox"/>	System and method for virus checking software	714/38
17	US 20030 10133 0 A1	<input checked="" type="checkbox"/>	System and method for dynamically patching code	712/32

	Document ID	U	Title	Current OR
18	US 20030 09365 0 A1	<input checked="" type="checkbox"/>	System and method for decoding and executing program binaries	712/209
19	US 20030 05568 8 A1	<input checked="" type="checkbox"/>	Arrangement and method for supporting temporary replacement	705/4
20	US 20030 05122 0 A1	<input checked="" type="checkbox"/>	Logical circuit delay optimization system	716/12
21	US 20030 04605 8 A1	<input checked="" type="checkbox"/>	Translation text management system	704/7
22	US 20030 03359 3 A1	<input checked="" type="checkbox"/>	Dynamic execution layer interface for explicitly or transparently executing application or system binaries	717/138
23	US 20030 00976 8 A1	<input checked="" type="checkbox"/>	Television system	725/112
24	US 20020 19697 9 A1	<input checked="" type="checkbox"/>	Automatically extracting graphical bar codes	382/190
25	US 20020 17185 6 A1	<input checked="" type="checkbox"/>	Method for automatically detecting and processing binary postscript print jobs	358/1.1 3
26	US 20020 13357 6 A1	<input checked="" type="checkbox"/>	System with a server for verifying new components	709/221
27	US 20020 11220 0 A1	<input checked="" type="checkbox"/>	Automated analysis of kernel and user core files including searching, ranking, and recommending patch files	714/38
28	US 20020 10586 8 A1	<input checked="" type="checkbox"/>	Recording medium for storing defect management information for recording real time data, defect managing method therefor, and real time data recording method	369/47. 14
29	US 20020 09562 5 A1	<input checked="" type="checkbox"/>	Identifying field replaceable units responsible for faults detected with processor timeouts utilizing IPL boot progress indicator status	714/44
30	US 20020 09389 4 A1	<input checked="" type="checkbox"/>	Apparatus and method of interrupt detection in an optical disc environment	369/47. 25
31	US 20020 07829 6 A1	<input checked="" type="checkbox"/>	Method and apparatus for resynchronizing paired volumes via communication line	711/114
32	US 20020 06247 9 A1	<input checked="" type="checkbox"/>	Microcontroller with modifiable program	717/168
33	US 20020 00127 2 A1	<input checked="" type="checkbox"/>	Recording defect substitution method for a disc-shaped recording medium, and a recording and reproducing apparatus for a disc-shaped recording medium	369/53. 17
34	US 20010 04213 7 A1	<input checked="" type="checkbox"/>	Communications network system using gateway	709/248

	Document ID	U	Title	Current OR
35	US 20010 03965 4 A1	<input checked="" type="checkbox"/>	Compiler processing system for generating assembly program codes for a computer comprising a plurality of arithmetic units	717/160
36	US 20010 03745 0 A1	<input checked="" type="checkbox"/>	System and method for process protection	713/152
37	US 20010 01891 8 A1	<input checked="" type="checkbox"/>	Systems and methods for preventing automatic identification of re-used single use devices	128/897
38	US 67253 96 B2	<input checked="" type="checkbox"/>	Identifying field replaceable units responsible for faults detected with processor timeouts utilizing IPL boot progress indicator status	714/10
39	US 66938 75 B1	<input checked="" type="checkbox"/>	Communication network system	370/217
40	US 66788 70 B2	<input checked="" type="checkbox"/>	Logical circuit delay optimization system	716/6
41	US 66746 97 B1	<input checked="" type="checkbox"/>	Apparatus for managing defects and recording and/or reproducing real time data	369/47.14
42	US 66583 11 B1	<input checked="" type="checkbox"/>	Maintenance management system for press line	700/95
43	US 66474 37 B2	<input checked="" type="checkbox"/>	Method for automatically detecting and processing binary postscript print jobs	710/14
44	US 66432 32 B2	<input checked="" type="checkbox"/>	Recording medium for storing defect management information for recording real time data, defect managing method therefor, and real time data recording method	369/47.14
45	US 65913 73 B1	<input checked="" type="checkbox"/>	Uniform configuration controller for replicated component systems	714/7
46	US 65888 73 B1	<input checked="" type="checkbox"/>	Printing apparatus and method	347/23
47	US 65804 39 B1	<input checked="" type="checkbox"/>	Graphical user interface for providing component relational information for a uniform configuration controller for replicated component systems	345/735
48	US 65785 79 B2	<input checked="" type="checkbox"/>	Systems and methods for preventing automatic identification of re-used single use devices	128/897
49	US 65781 58 B1	<input checked="" type="checkbox"/>	Method and apparatus for providing a raid controller having transparent failover and fallback	714/11
50	US 65498 86 B1	<input checked="" type="checkbox"/>	System for lost packet recovery in voice over internet protocol based on time domain interpolation	704/270.1
51	US 65494 99 B2	<input checked="" type="checkbox"/>	Recording defect substitution method for a disc-shaped recording medium, and a recording and reproducing apparatus for a disc-shaped recording medium	369/53.16
52	US 65046 46 B1	<input checked="" type="checkbox"/>	Optical communication system and component control architectures and methods	359/341.1
53	US 64964 55 B1	<input checked="" type="checkbox"/>	Recording defect substitution method for a disc-shaped recording medium, and a recording and reproducing apparatus for a disc-shaped recording medium	369/47.14
54	US 64804 46 B1	<input checked="" type="checkbox"/>	Recording medium for storing defect management information for recording real time data, defect managing method therefor and real time data recording method	369/47.14
55	US 64635 59 B1	<input checked="" type="checkbox"/>	Non-volatile fault indicator	714/724
56	US 63775 24 B1	<input checked="" type="checkbox"/>	Recording medium for storing defect management information for recording real time data, defect managing method therefor, and real time data recording method	369/47.14

	Document ID	U	Title	Current OR
57	US 63752 99 B1	<input checked="" type="checkbox"/>	Faulty ink ejector detection in an ink jet printer	347/19
58	US 63699 22 B1	<input checked="" type="checkbox"/>	Wavelength division multiplex optical transmission apparatus	398/79
59	US 63597 29 B1	<input checked="" type="checkbox"/>	Optical communication system and component control architectures and methods	359/341 .1
60	US 63514 47 B1	<input checked="" type="checkbox"/>	Recording defect substitution method for a disc-shaped recording medium, and a recording and reproducing apparatus for a disc-shaped recording medium	369/275 .3
61	US 63140 78 B1	<input checked="" type="checkbox"/>	Recording defect substitution method for a disc-shaped recording medium, and a recording and reproducing apparatus for a disc-shaped recording medium	369/275 .3
62	US 63012 20 B1	<input checked="" type="checkbox"/>	Recording defect substitution method for a disc-shaped recording medium, and a recording and reproducing apparatus for a disc-shaped recording medium	369/275 .3
63	US 62755 59 B1	<input checked="" type="checkbox"/>	Method and system for diagnosing faults in imaging scanners	378/4
64	US 62435 30 B1	<input checked="" type="checkbox"/>	Recording device, video output device, video display/record system and signal processing method for video display/record system	386/94
65	US 62405 31 B1	<input checked="" type="checkbox"/>	System and method for computer operating system protection	714/38
66	US 62376 04 B1	<input checked="" type="checkbox"/>	Systems and methods for preventing automatic identification of re-used single use devices	128/897
67	US 61957 32 B1	<input checked="" type="checkbox"/>	Storage device capacity management	711/156
68	US 61822 87 B1	<input checked="" type="checkbox"/>	Preferred service management system for a multimedia video decoder	725/48
69	US 61822 39 B1	<input checked="" type="checkbox"/>	Fault-tolerant codes for multi-level memories	714/5
70	US 61785 49 B1	<input checked="" type="checkbox"/>	Memory writer with deflective memory-cell handling capability	717/124
71	US 61755 49 B1	<input checked="" type="checkbox"/>	Recording defect substitution method of a disc-shaped recording medium, and a recording and reproducing apparatus for a disc-shaped recording medium	369/53. 15
72	US 61342 14 A	<input checked="" type="checkbox"/>	Method for substituting defective recording of discoid recording medium and discoid recording medium recording and reproducing device	369/47. 14
73	US 60724 02 A	<input checked="" type="checkbox"/>	Secure entry system with radio communications	340/5.2 8
74	US 59918 56 A	<input checked="" type="checkbox"/>	System and method for computer operating system protection	711/147
75	US 59742 40 A	<input checked="" type="checkbox"/>	Method and system for buffering condition code data in a data processing system having out-of-order and speculative instruction execution	712/218
76	US 59681 59 A	<input checked="" type="checkbox"/>	Interrupt system with fast response time	710/264
77	US 59152 36 A	<input checked="" type="checkbox"/>	Word recognition system which alters code executed as a function of available computational resources.	704/251
78	US 58988 43 A	<input checked="" type="checkbox"/>	System and method for controlling device which is present in media console and system unit of a split computer system	710/100
79	US 58929 44 A	<input checked="" type="checkbox"/>	Program execution and operation right management system suitable for single virtual memory scheme	718/100

	Document ID	U	Title	Current OR
80	US 58782 09 A	<input checked="" type="checkbox"/>	Apparatus and method for identifying a failed subscriber unit in a wireless communication system	714/57
81	US 58706 01 A	<input checked="" type="checkbox"/>	Data processing apparatus and method for correcting faulty microcode in a ROM device via a flag microinstruction in a RAM device including corrected microcode	712/244
82	US 58623 70 A	<input checked="" type="checkbox"/>	Data processor system with instruction substitution filter for deimplementing instructions	712/226
83	US 58225 70 A	<input checked="" type="checkbox"/>	System and method for parsing and executing a single instruction stream using a plurality of tightly coupled parsing and execution units	703/26
84	US 58155 57 A	<input checked="" type="checkbox"/>	Homeowner key for an electronic real estate lockbox system	340/5.6 4
85	US 57907 78 A	<input checked="" type="checkbox"/>	Simulated program execution error detection method and apparatus	714/38
86	US 57882 88 A	<input checked="" type="checkbox"/>	Sealing arrangement	285/93
87	US 57674 98 A	<input checked="" type="checkbox"/>	Bar code error scanner	235/462 .16
88	US 57059 91 A	<input checked="" type="checkbox"/>	Access control device featuring key ordering or key simultaneity	340/5.2 8
89	US 56756 45 A	<input checked="" type="checkbox"/>	Method and apparatus for securing executable programs against copying	713/187
90	US 55903 15 A	<input checked="" type="checkbox"/>	Method and apparatus for simulating user input device presence in a computer system	703/25
91	US 55641 83 A	<input checked="" type="checkbox"/>	Producing system of printed circuit board and method therefor	29/840
92	US 55308 61 A	<input checked="" type="checkbox"/>	Process enactment and tool integration via a task oriented paradigm	705/8
93	US 55287 50 A	<input checked="" type="checkbox"/>	Method/apparatus for recovering from a process within the system kernel without creating a recovery environment	714/15
94	US 55242 32 A	<input checked="" type="checkbox"/>	Device for receiving and adapting a set of computer memory modules to a single computer memory module receiving socket	711/115
95	US 55049 20 A	<input checked="" type="checkbox"/>	Video driver system for communicating device specific primitive commands to multiple video controller types	712/42
96	US 54103 02 A	<input checked="" type="checkbox"/>	Method and apparatus for processing a received message in a communication receiver	340/7.5 2
97	US 54044 99 A	<input checked="" type="checkbox"/>	Semi-automatic program execution error detection	714/54
98	US 52789 81 A	<input checked="" type="checkbox"/>	Character stream search apparatus using a finite state automation	707/6
99	US 52126 93 A	<input checked="" type="checkbox"/>	Small programmable array to the on-chip control store for microcode correction	714/5
100	US 51858 69 A	<input checked="" type="checkbox"/>	System for masking execution ready signal during unsettled period of determining branch condition to prevent reading out of stored instructions	712/234
101	US 51792 41 A	<input checked="" type="checkbox"/>	Apparatus for determining tonality for chord progression	84/613
102	US 51076 67 A	<input checked="" type="checkbox"/>	Spinning frame management method	57/264

	Document ID	U	Title	Current OR
103	US 50606 04 A	<input checked="" type="checkbox"/>	Method of detecting failure of a valve timing changeover control system of an internal combustion engine	123/90. 16
104	US 50518 86 A	<input checked="" type="checkbox"/>	System for character stream search using finite state automaton technique	707/6
105	US 50332 90 A	<input checked="" type="checkbox"/>	Method of detecting failure of a valve timing changeover control system of an internal combustion engine	73/118. 1
106	US 48902 27 A	<input checked="" type="checkbox"/>	Autonomous resource management system with recorded evaluations of system performance with scheduler control including knowledge learning function	706/14
107	US 48291 63 A	<input checked="" type="checkbox"/>	Crack detector for heated glass panel	219/547
108	US 47976 57 A	<input checked="" type="checkbox"/>	Portable self-contained intrusion detector for passenger aircraft	340/541
109	US 47275 45 A	<input checked="" type="checkbox"/>	Method and apparatus for isolating faults in a digital logic circuit	714/33
110	US 46673 26 A	<input checked="" type="checkbox"/>	Method and apparatus for error detection and correction in systems comprising floppy and/or hard disk drives	714/762
111	US 43664 65 A	<input checked="" type="checkbox"/>	False failure detection prevention in an air bag diagnostic system	280/735
112	US 43597 15 A	<input checked="" type="checkbox"/>	Coded failure indicator for an air bag system	280/735
113	US 43386 60 A	<input checked="" type="checkbox"/>	Relational break signal generating device	714/34
114	US 43075 14 A	<input checked="" type="checkbox"/>	Bore diameter measurement gage	33/543. 1
115	US 40178 94 A	<input checked="" type="checkbox"/>	Method for preparing color separation printing patterns	358/515
116	US 37959 16 A	<input type="checkbox"/>	PROCESS OPERATION ERROR MONITOR AND ERROR MESSAGE SYSTEM	714/55